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FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. *RAB 2001-1*	SERIAL NO. *09/757,439*
LIST OF PRIOR ART CITED BY APPLICANT *(Use several sheets if necessary)*		APPLICANT *Richard A. Belgard*	
		FILING DATE *01/10/2001*	GROUP: 2185 2147

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
✓	4	0	8	4	2	2	5	4/11/78	Anderson et. al	711	206	9/24/76
	4	4	0	0	7	7	4	8/23/83	Toy	711	3	2/2/81
	5	1	6	5	0	2	8	11/17/92	Zulian	711	3	3/7/89
	5	3	2	1	8	3	6	6/14/94	Crawford, et al.	711	206	4/9/90
	5	3	3	5	3	3	3	8/2/94	Hinton, et al.	711	207	10/29/91
✓	5	4	2	3	0	1	4	6/6/95	Hinton, et al.	711	3	2/24/94

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
												YES	NO
✓	0	6	6	8	5	6	5	8/23/95	EPO	G06F	12/10		NONE

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

✓			*U5S 486 Green CPU*, United Microelectronics Corporation, 1994-95, pp. 3-1 to 3-26.
✓			*Intel Microprocessors*, Vol. 1, Intel Corporation, 1993, pp. 2-229 to 2-287.
✓			*Computer Architecture A Quantitative Approach*, Hennessey and Patterson, pp. 432-497 (1990).

EXAMINER	DATE CONSIDERED
	8/28/01

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

MN			Hua, A. Hunt, L. Liu, J-K Peir, D. Pruett, and J. Temple, "*Early Resolution of Address Translation in Cache Design*," Proc. of Int'l Conf. on Computer Designs, Oct. 1990, pp. 408–412.
MN			*DPS-8 Assembly Instructions*, Honeywell Corporation, April, 1980, Chapters 3 and 5.
MN			*The Multics System*, Elliot Organick, 1972, pp. 6-7, 38-51.

EXAMINER	DATE CONSIDERED
	8/29/01

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